

# TASKING SOLUTIONS FOR INFINEON AURIX™ TC4X MICROCONTROLLERS



TASKING SMARTCODE - NEW MULTI-CORE DEVELOPMENT ENVIRONMENT	BASE EDITION	PERFORMANCE EDITION
Eclipse-based 64-bit application software design environment for Infineon AURIX™ TC4x family	●	●
C/C++ Compiler toolset for TriCore TCv1.8 realtime cores	●	●
C/C++ Compiler toolset for TriCore TCv1.8 cyber security realtime module (CSRM)	●	●
C Compiler toolset for AURIX™ TC4x Standby Controller (SCR)		●
C Compiler toolset for MCS/GTM v4.x		●
C/C++ Compiler toolset for AURIX™ TC4x PPU (ARC EV7xFS)		●
PPU Run Time Environment library <b>(1)</b>		●
Simulator Debugger for:		
• TriCore TCv1.8	●	●
• CSRM	●	●
• PPU		●
• SCR		●
• GTM-MCS		●
Hardware OCDS Debugger for TriCore TCv1.8 cores and FLASH programming <b>(2)</b>		●
Hardware OCDS Debugger for AURIX™ TC4x PPU and FLASH programming <b>(3)</b>		●
Integrated MISRA C Coding analyzer	●	●
Integrated CERT C Secure Coding analyzer	●	●

(1) Library to communicate between the TriCore processor core and the PPU

(2) Eclipse plug-in allows access to powerful and versatile TASKING winIDEA IDE to connect to target ECU via DAP miniWiggler or via TASKING BlueBox iC7mini (available separately)

(3) Eclipse plug-in allows access to powerful and versatile TASKING winIDEA IDE to connect to target ECU via TASKING BlueBox iC7mini (available separately)

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Tool Qualification for TriCore TCv1.8 compiler toolset (ISO2626 up to ASIL D, IEC61508 up to SIL 3)	●	●
Tool Qualification for PPU compiler toolset (ISO2626 up to ASIL D, IEC61508 up to SIL 3)		●
Qualified TriCore run-time libraries, and floating-point libraries according to ISO26262 up to ASIL D	●	●
Qualified PPU run-time libraries, and floating-point libraries according to ISO26262 up to ASIL D		●
Cybersecurity conform according to ISO/SAE 21434:2021 road vehicles - cybersecurity engineering	●	●
Certified for the development of safety critical software applications by safety experts from TÜV-Nord <b>(4)</b>	●	●

(4) Expected to be available in Q2/2024